

ABSTRACT

A problem to be solved by the present invention is to eliminate variation in potential in a turn-off time period of each GTO element, and to stabilize a gate drawing current by surely performing the turn-off of the GTO element.

In an inverter apparatus (11) having a three-phase inverter (14) configured to include paired GTO elements UP, UN, VP, VN, WP, and WN connected in a bridge configuration and to convert a power supply voltage, which is supplied from a dc power supply (13), by the GTO elements UP, UN, VP, VN, WP, and WN into an ac voltage, an inverter control portion has a simultaneous switching prevention function of delaying a turn-on operation of each of the GTO elements VN and WN, which correspond to phases other than a phase corresponding to an optional one of the GTO elements and also correspond to an electrode opposite to an electrode corresponding to the optional one of the GTO elements, for example, the GTO element UP, by a predetermined time in a case where a turn-on command signal for turning on each of the GTO elements VN and WN, which correspond to the other phases, is generated within a predetermined time period since the turn-off of the optional one of the GTO elements.